

FIG. -1

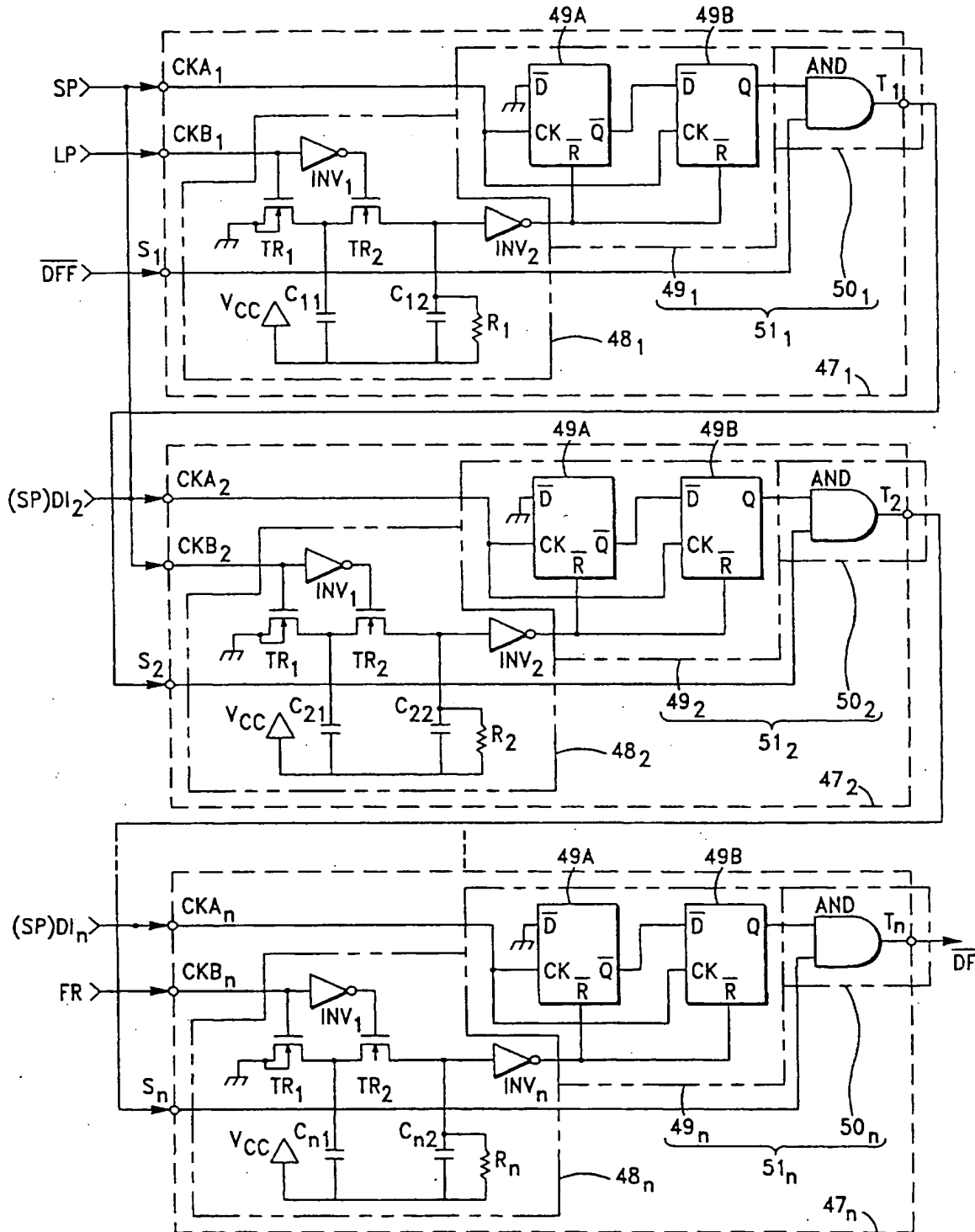


FIG.-2

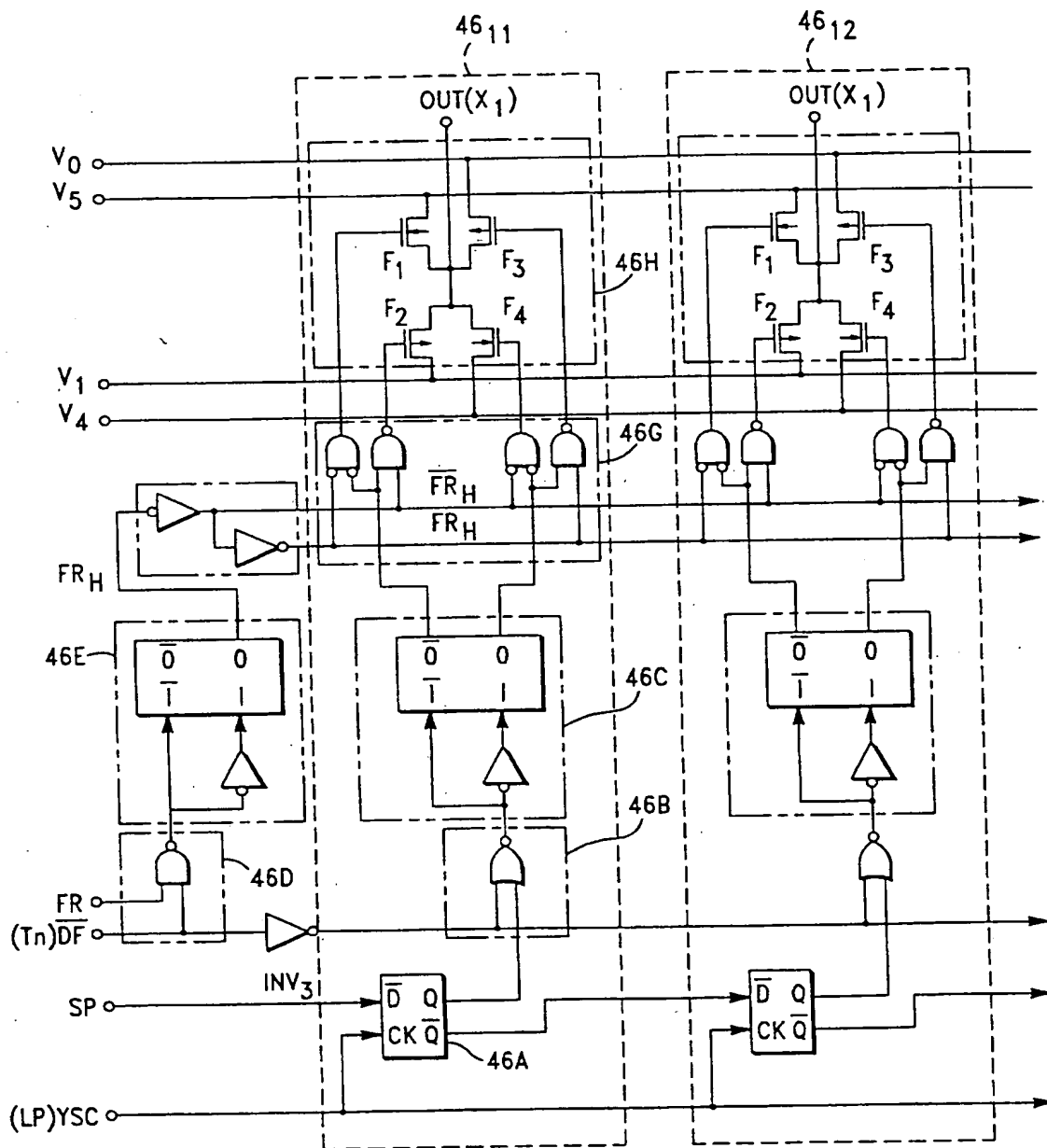
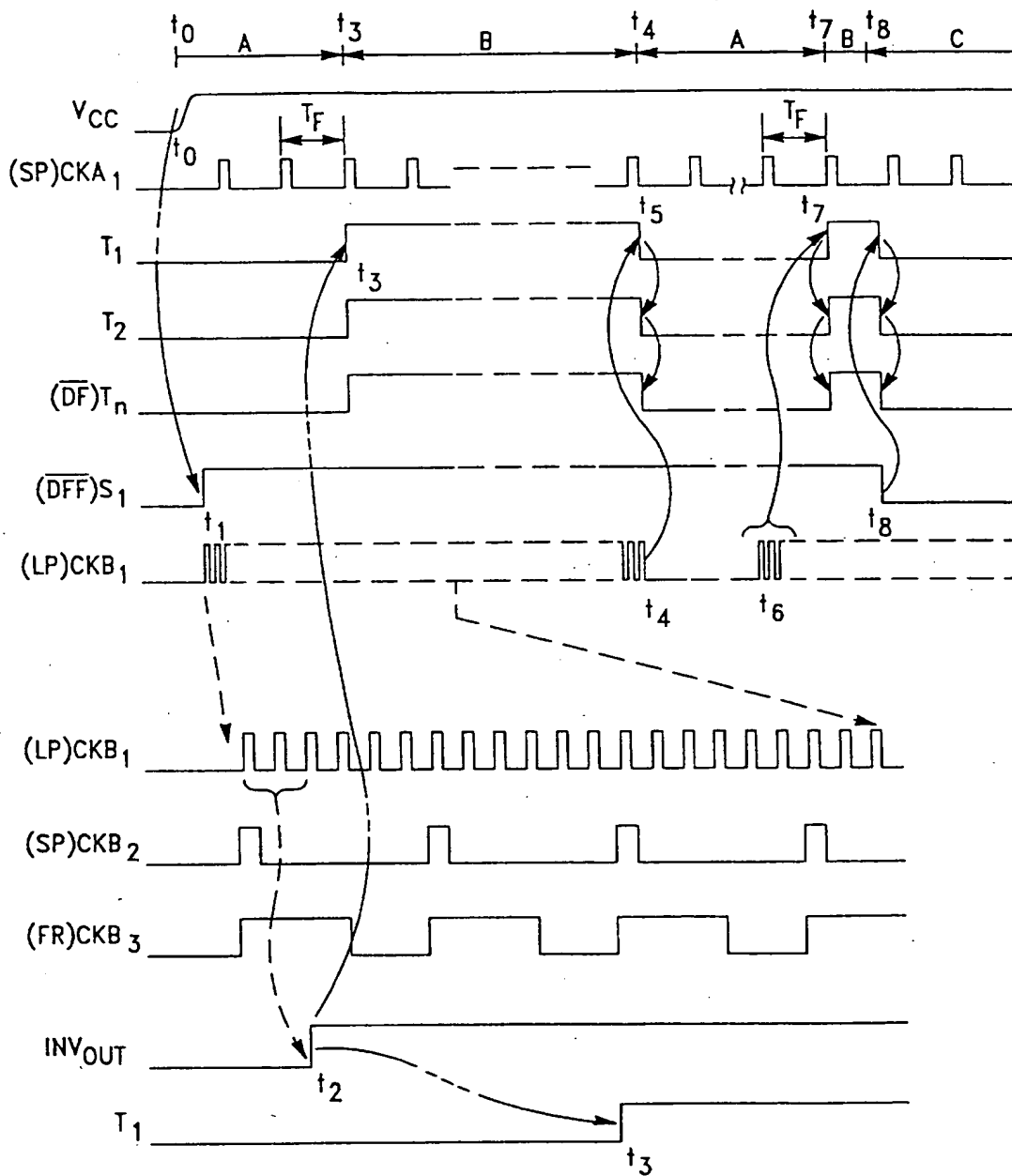


FIG.-3



A: LIQUID CRYSTAL DRIVE INHIBIT PERIOD
 B: LIQUID CRYSTAL DRIVE PERIOD
 C: DISPLAY-OFF PERIOD

FIG.-4

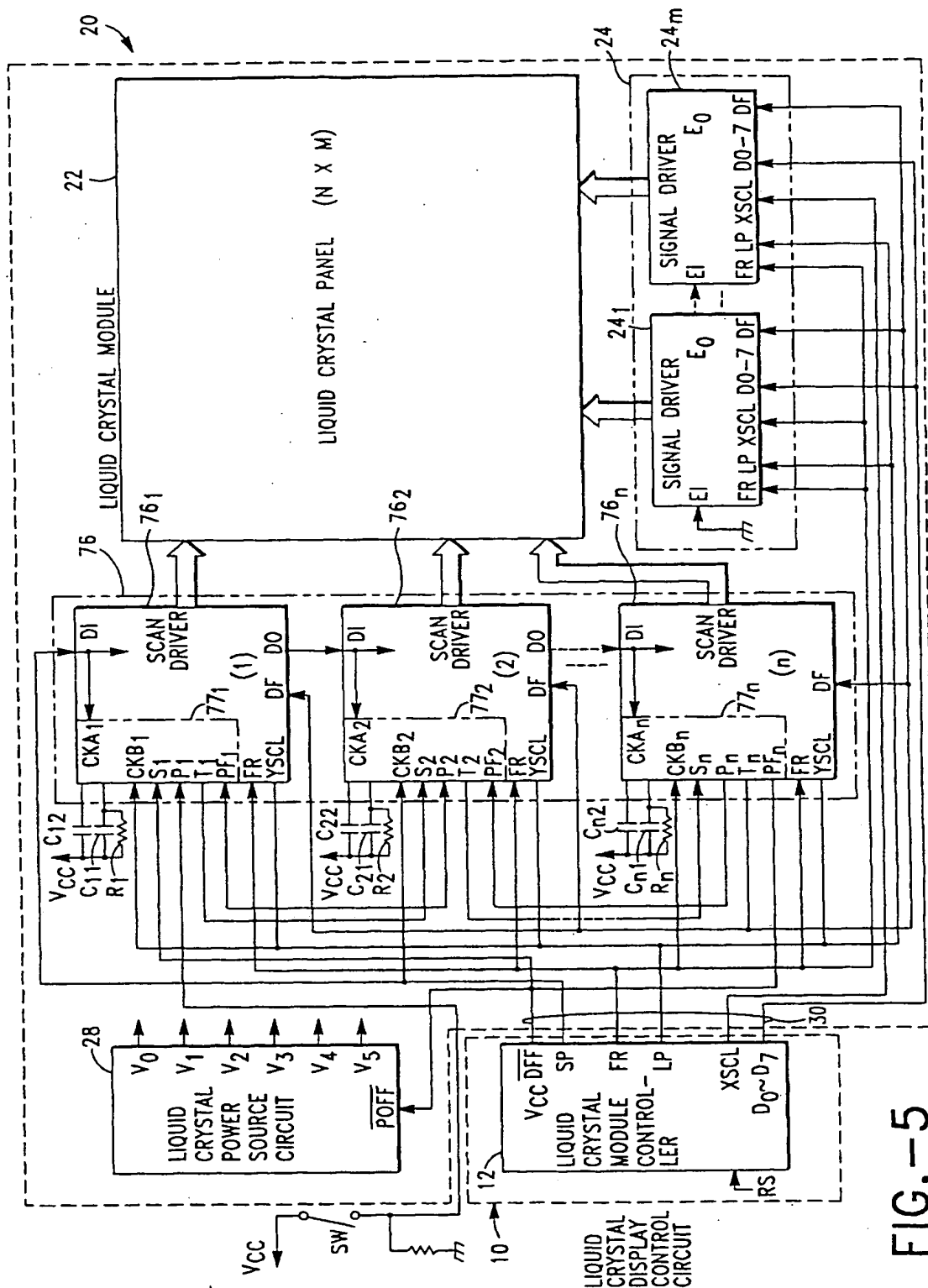


FIG. 5

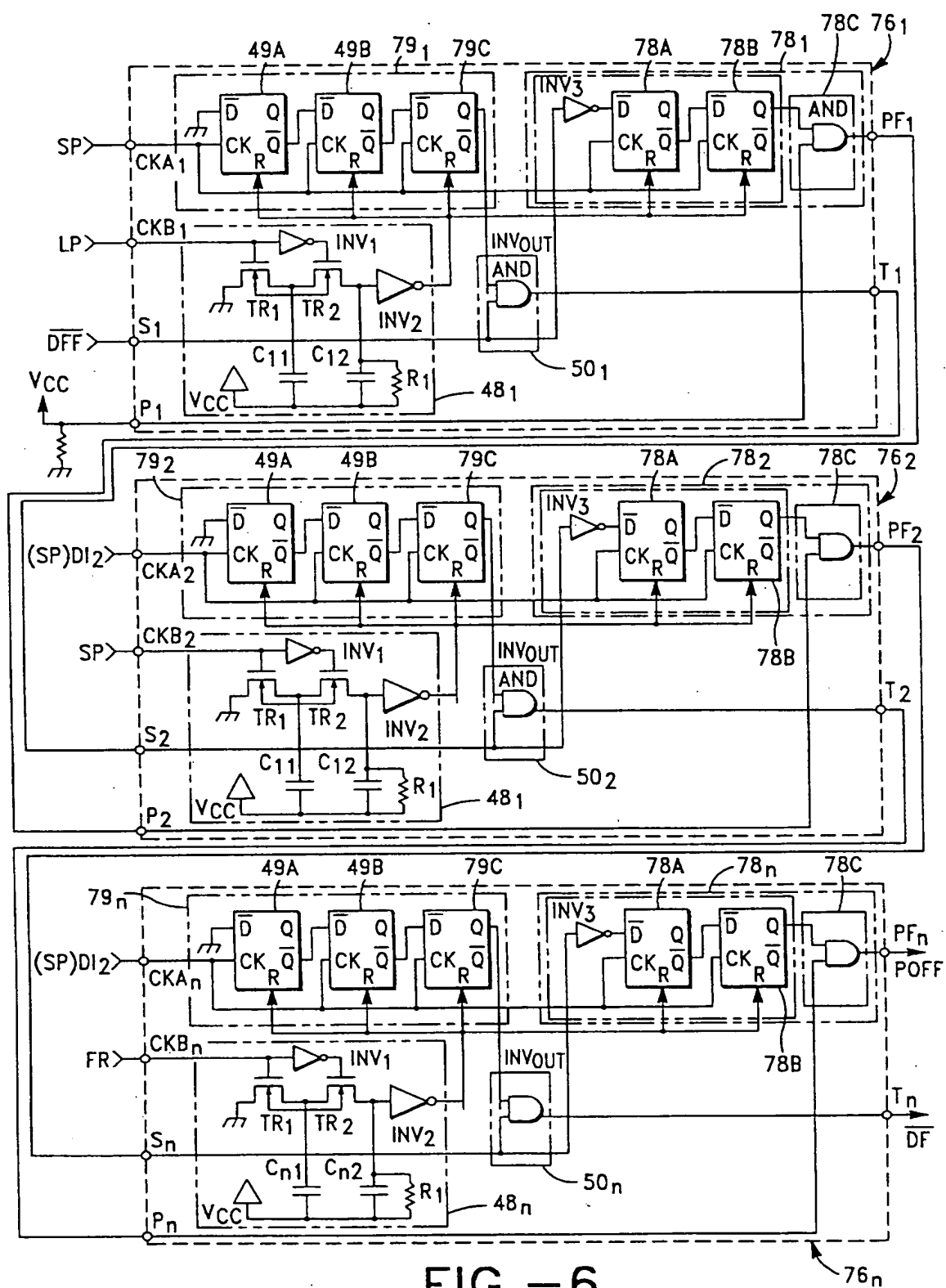


FIG.-6

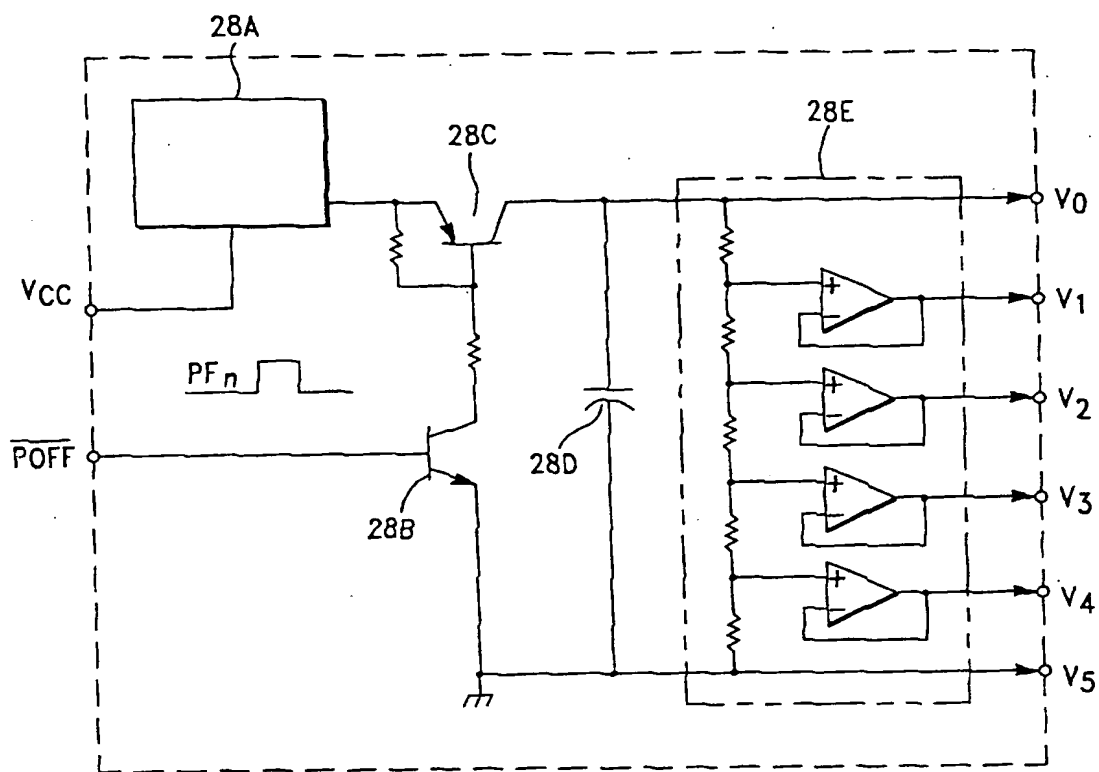
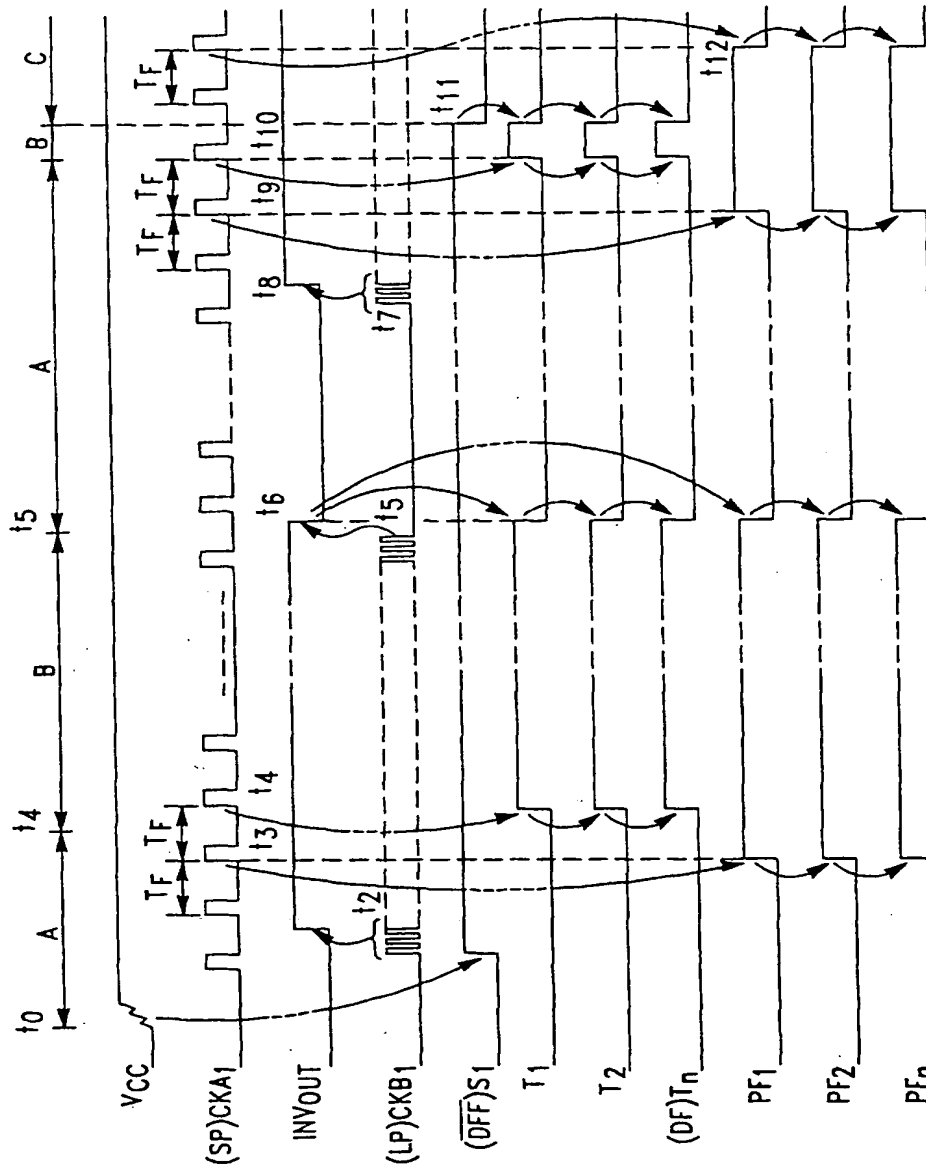


FIG.-7
(PRIOR ART)

FIG. 7 is a block diagram of a circuit 700.



A: LIQUID CRYSTAL DRIVE INHIBIT CIRCUIT
B: LIQUID CRYSTAL DRIVE PERIOD
C: DISPLAY-OFF PERIOD

FIG.-8

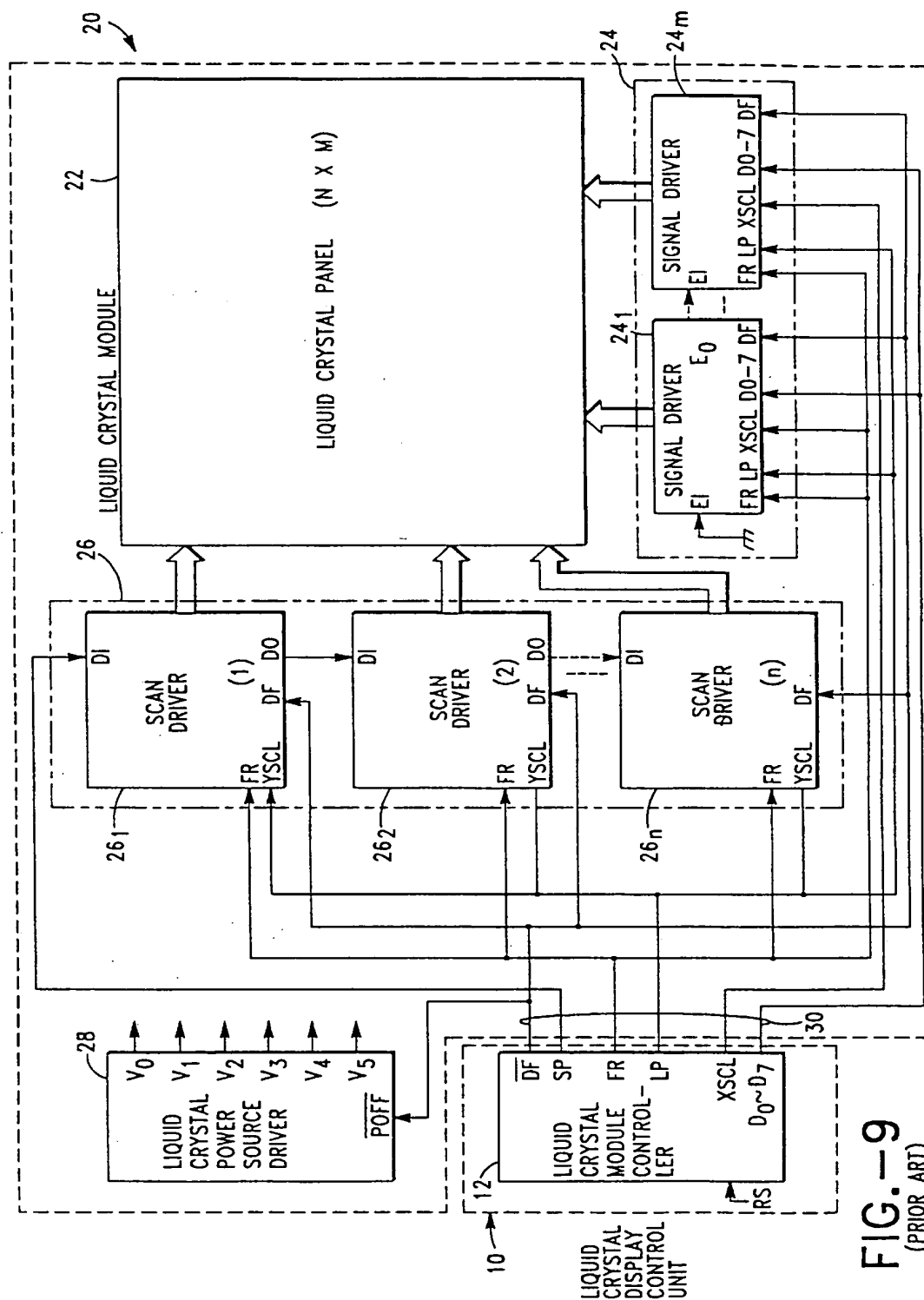


FIG. -9
(PRIOR ART)